

APPENDIX A
"CLEAN" VERSION OF EACH PARAGRAPH/SECTION/CLAIM
37 C.F.R. § 1.121(b)(ii) AND (c)(i)

CLAIMS (with indication of amended or new):

AMENDED

3. A delay line comprising:
a dielectric substrate including a pair of main surfaces;
a transmission line disposed on one of the main surfaces of the dielectric substrate;
a ground conductor disposed on the other of the main surfaces of the dielectric substrate; and
an adjustable capacitance being disposed on the dielectric substrate and connected to the
transmission line for setting a desired delay time of the delay line, wherein said capacitance is
provided by a variable capacitor.

AMENDED

4. A delay line comprising:
a dielectric substrate including a pair of main surfaces;
a transmission line disposed on one of the main surfaces of the dielectric substrate;
a ground conductor disposed on the other of the main surfaces of the dielectric substrate; and
an adjustable capacitance being disposed on the dielectric substrate and connected to the
transmission line for setting a desired delay time of the delay line, wherein said capacitance is
provided by a varicap diode.

AMENDED

5. A delay line comprising:
a dielectric substrate including a pair of main surfaces;
a transmission line disposed on one of the main surfaces of the dielectric substrate;
a ground conductor disposed on the other of the main surfaces of the dielectric substrate; and

a capacitance being disposed on the dielectric substrate and connected to the transmission line for setting a desired delay time of the delay line, wherein said capacitance is provided by a diode.

AMENDED

7. A delay line comprising:
a dielectric substrate including a pair of main surfaces;
a transmission line disposed on one of the main surfaces of the dielectric substrate;
a ground conductor disposed on the other of the main surfaces of the dielectric substrate; and
a capacitance being disposed on the dielectric substrate and connected to the transmission line for setting a desired delay time of the delay line, wherein said capacitance is connected in parallel to the transmission line.

AMENDED

9. A delay line comprising:
a multilayer structure formed by laminating a plurality of dielectric layers;
a transmission line formed on a dielectric layer embedded in the multilayer structure;
a plurality of ground conductors disposed on the dielectric layers and a pair of said ground conductors being disposed on opposite sides of the transmission line; and
a capacitance disposed on the multilayer structure and connected to the transmission line for setting a desired delay time of the delay line, wherein said capacitance is adjustable.

AMENDED

13. A delay line comprising:
a multilayer structure formed by laminating a plurality of dielectric layers;
a transmission line formed on a dielectric layer embedded in the multilayer structure;
a plurality of ground conductors disposed on the dielectric layers and a pair of said ground conductors being disposed on opposite sides of the transmission line; and

a capacitance disposed on the multilayer structure and connected to the transmission line for
setting a desired delay time of the delay line, wherein said capacitance is provided by a diode.